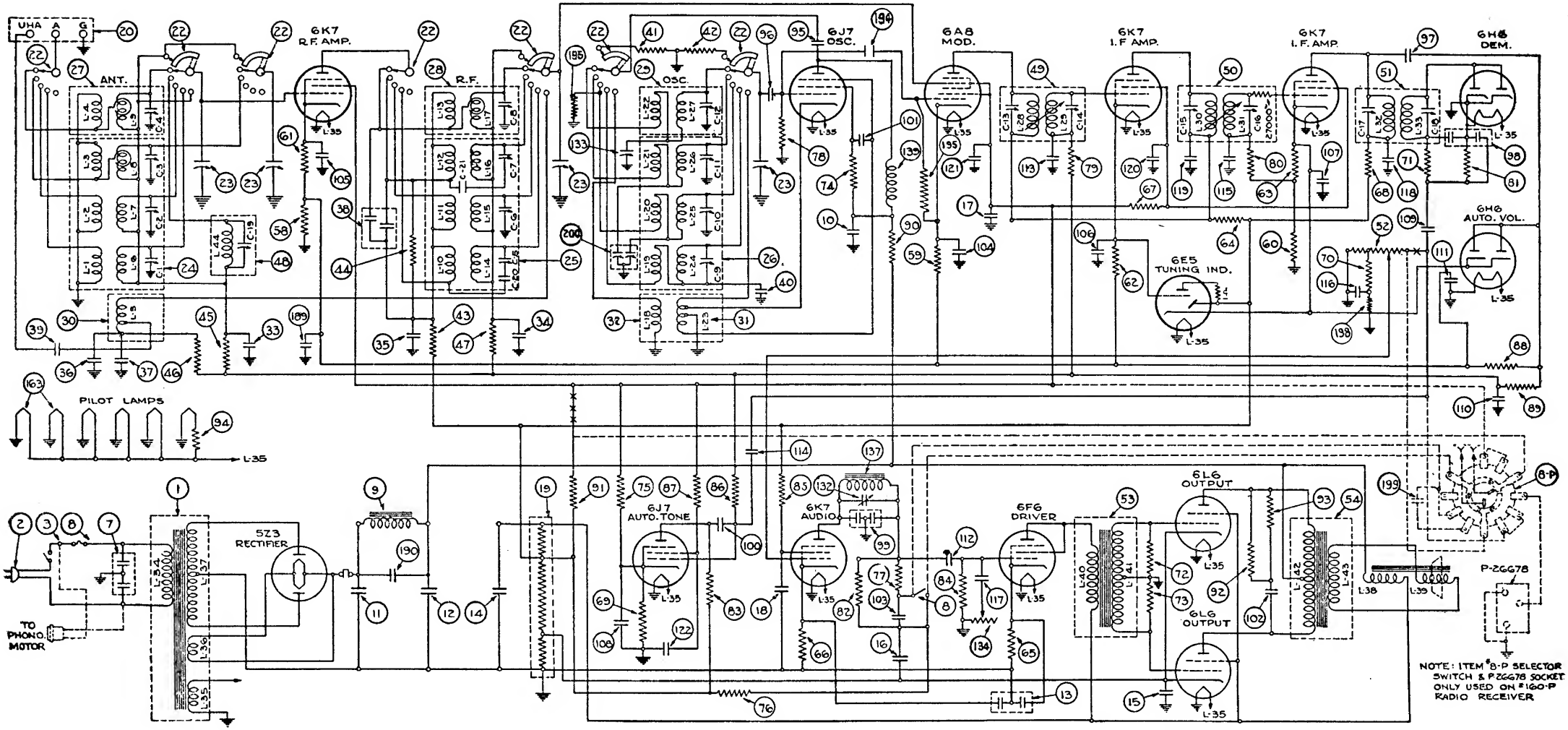

Fig. 2. Schematic Circuit of Receiver.

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